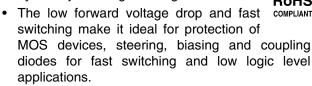


#### **Vishay Semiconductors**

# **Small Signal Schottky Diodes**

#### **Features**

- · For general purpose applications
- The LL101 series is a metal-on-silicon Schottky barrier device which is protected by a PN junction guard ring.





- Low capacitance
- · Low leakage current
- This diode is also available in the DO-35 case with type designation SD101A, B, C and in the SOD-123 case with type designation SD101AW-V, SD101BW-V, SD101CW-V
- · AEC-Q101 qualified
- Compliant to RoHS directive 2002/95/EC and in accordance to WEEE 2002/96/EC

#### **Applications**

- HF-Detector
- Protection circuit
- Diode for low currents wits a low supply voltage
- Small battery charger
- Power supplies
- DC/DC converter for notebooks

#### **Parts Table**

Part	Type differentiation	Ordering code	Remarks
LL101A	$V_R = 60$ V, $V_F$ at $I_F$ 1 mA max. 410 mV	LL101A-GS18 or LL101A-GS08	Tape and Reel
LL101B	$V_R = 50 \text{ V}, V_F \text{ at } I_F 1 \text{ mA max. } 400 \text{ mV}$	LL101B-GS18 or LL101B-GS08	Tape and Reel
LL101C	$V_R = 40 \text{ V}, V_F \text{ at } I_F 1 \text{ mA max. } 390 \text{ mV}$	LL101C-GS18 or LL101C-GS08	Tape and Reel

## **Absolute Maximum Ratings**

 $T_{amb} = 25$  °C, unless otherwise specified

Parameter	Test condition	Part	Symbol	Value	Unit
		LL101A	V <sub>RRM</sub>	60	V
Peak inverse voltage		LL101B	V <sub>RRM</sub>	50	V
		LL101C	V <sub>RRM</sub>	40	V
Power dissipation (infinite heatsink)			P <sub>tot</sub>	400 <sup>1)</sup>	mW
Forward continuous current			I <sub>F</sub>	30	mA
Maximum single cycle surge 10 μs square wave			I <sub>FSM</sub>	2	А

<sup>1)</sup> Valid provided that electrodes are kept at ambient temperature



#### **Mechanical Data**

Case: MiniMELF SOD-80
Weight: approx. 31 mg
Cathode band color: black
Packaging codes/options:

94 9371

GS18 / 10 k per 13" reel (8 mm tape), 10 k/box GS08 / 2.5 k per 7" reel (8 mm tape), 12.5 k/box

# LL101A, LL101B, LL101C

# **Vishay Semiconductors**



#### **Thermal Characteristics**

T<sub>amb</sub> = 25 °C, unless otherwise specified

Parameter	Test condition	Symbol	Value	Unit	
Junction temperature		T <sub>j</sub>	125	°C	
Storage temperature range		T <sub>stg</sub>	- 65 to + 150	°C	
Thermal resistance junction to ambient air	on PC board 50 mm x 50 mm x 1.6 mm	R <sub>thJA</sub>	320	K/W	

#### **Electrical Characteristics**

T<sub>amb</sub> = 25 °C, unless otherwise specified

Parameter	Test condition	Part	Symbol	Min	Тур.	Max	Unit
Reverse Breakdown Voltage	I <sub>R</sub> = 10 μA	LL101A	V <sub>(BR)</sub>	60			V
		LL101B	V <sub>(BR)</sub>	50			V
		LL101C	V <sub>(BR)</sub>	40			V
Leakage current	V <sub>R</sub> = 50 V	LL101A	I <sub>R</sub>			200	nA
	V <sub>R</sub> = 40 V	LL101B	I <sub>R</sub>			200	nA
	V <sub>R</sub> = 30 V	LL101C	I <sub>R</sub>			200	nA
Forward voltage drop	I <sub>F</sub> = 1 mA	LL101A	$V_{F}$			410	mV
	I <sub>F</sub> = 1 mA	LL101B	$V_{F}$			400	mV
	I <sub>F</sub> = 1 mA	LL101C	V <sub>F</sub>			390	mV
	I <sub>F</sub> = 15 mA	LL101A	V <sub>F</sub>			1000	mV
		LL101B	V <sub>F</sub>			950	mV
		LL101C	V <sub>F</sub>			900	mV
Diode capacitance	V <sub>R</sub> = 0 V, f = 1 MHz	LL101A	C <sub>D</sub>			2.0	pF
	V <sub>R</sub> = 0 V, f = 1 MHz	LL101B	C <sub>D</sub>			2.1	pF
		LL101C	C <sub>D</sub>			2.2	pF
Reverse recovery time	I <sub>F</sub> = I <sub>R</sub> = 5 mA, recover to 0.1 I <sub>R</sub>		t <sub>rr</sub>			1	ns

## **Typical Characteristics**

T<sub>amb</sub> = 25 °C, unless otherwise specified

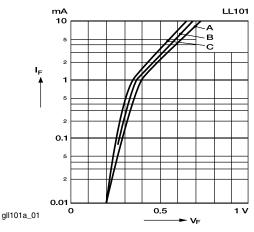


Figure 1. Typ.  $I_F$  vs.  $V_F$  for primary conduction through the Schottky barrier

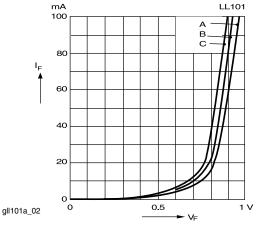
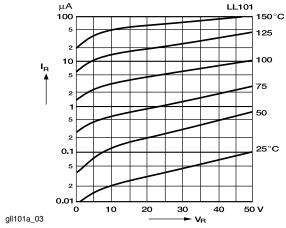


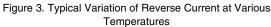
Figure 2. Typ.  $I_F$  of combination Schottky barrrier and PN junction guard ring





# **Vishay Semiconductors**





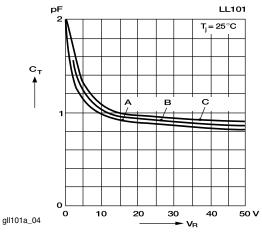
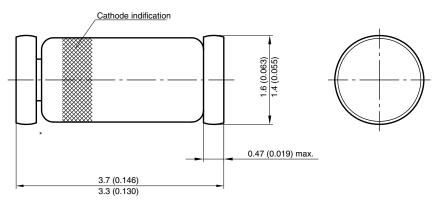
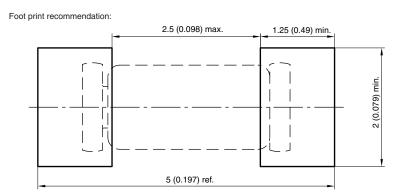


Figure 4. Typical Capacitance Curve as a Function of Reverse Voltage

## Package Dimensions in millimeters (inches): MiniMELF SOD-80



<sup>\*</sup> The gap between plug and glass can be either on cathode or anode side



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96 12070



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